

What is claimed is:

- 1 1. An apparatus comprising:
  - 2 a thermal mass having a cavity formed within the mass;
  - 3 inlet and outlet ports formed within the thermal mass and coupling the cavity
  - 4 with regions outside the thermal mass; and
  - 5 a folded fin located within the cavity, said folded fin defining, at least in
  - 6 part, a plurality of microchannels within the cavity.
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- 1 2. The apparatus of claim 1 wherein the folded fin comprises aluminum.
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- 1 3. The apparatus of claim 1 wherein the folded fin comprises copper.
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- 1 4. The apparatus of claim 3 wherein the thermal mass comprises copper and  
2 wherein the folded fin is physically coupled to the thermal mass by brazing.
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- 1 5. An apparatus comprising:
  - 2 an integrated circuit (IC) die; and
  - 3 a thermal mass coupled to the IC die, the thermal mass comprising:
    - 4 a cavity; and
    - 5 a folded fin located within the cavity, wherein the folded fin defines,
    - 6 at least in part, a plurality of microchannels within the cavity.
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- 1 6. The apparatus of claim 5, further comprising  
2 a solderable layer formed on the IC die; wherein the thermal mass is metallic  
3 and wherein the thermal mass is thermally and operatively coupled to IC die by the  
4 solderable layer.
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1       7.     The apparatus of claim 6, wherein the solderable layer is formed from at  
2     least one of the following metals: copper (Cu), gold (Au), nickel (Ni), aluminum  
3     (Al), titanium (Ti), tantalum (Ta), silver (Ag) and Platinum (Pt).

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1       8.     The apparatus of claim 6, wherein the solderable layer and the metallic  
2     thermal mass are made of substantially similar metals.

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1       9.     The apparatus of claim 5, wherein the thermal mass is thermally and  
2     operatively coupled to the IC die by a thermal adhesive disposed between the  
3     thermal mass and the surface of the IC die.

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1       10.    The apparatus of claim 5, wherein the thermal mass is thermally coupled to  
2     the IC die by a thermal interface material (TIM) layer.

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1       11.    The apparatus of claim 10, further comprising a substrate to which the IC die  
2     is flip-bonded.

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1       12.    The apparatus of claim 11, wherein the thermal mass is operatively coupled  
2     to the substrate via a plurality of fasteners.

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1       13.    The apparatus of claim 12, further comprising a plurality of standoffs  
2     physically coupled to the substrate and to which the plurality of fasteners are  
3     physically coupled.

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1       14.    An apparatus comprising:

2              an integrated circuit (IC) package, said IC package containing one or more  
3     IC dies; and

4              a thermal mass coupled to the IC package, the thermal mass comprising:  
5                  a cavity; and

6                   a folded fin located within the cavity, the folded fin defining, at least  
7                   in part, a plurality of microchannels within the cavity.

1                 15. The apparatus of claim 14, further comprising:

2                   a solderable layer formed on the IC package, wherein the thermal mass is  
3                   metallic and wherein the thermal mass is thermally and operatively coupled to IC  
4                   package by the solderable layer.

1                 16. The apparatus of claim 15, wherein the solderable layer is formed from at  
2                   least one of the following metals: copper (Cu), gold (Au), nickel (Ni), aluminum  
3                   (Al), titanium (Ti), tantalum (Ta), silver (Ag) and Platinum (Pt).

1                 17. The apparatus of claim 15, wherein the solderable layer and the metallic  
2                   thermal mass are made of substantially similar metals.

1                 18. The apparatus of claim 14, wherein the thermal mass is thermally and  
2                   operatively coupled to the IC package by a thermal adhesive disposed between the  
3                   thermal mass and the surface of the IC package.

1                 19. The apparatus of claim 14, wherein the thermal mass is thermally coupled to  
2                   the IC package by a thermal interface material (TIM) layer.

1                 20. The apparatus of claim 19, further comprising a substrate to which the IC  
2                   package is flip-bonded.

1                 21. The apparatus of claim 20, wherein the thermal mass is operatively coupled  
2                   to the substrate via a plurality of fasteners.

1       22. The apparatus of claim 21, further comprising a plurality of standoffs  
2 physically coupled to the substrate and to which the plurality of fasteners are  
3 physically coupled.

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2       23. A system, comprising:  
3           an integrated circuit (IC) die;  
4           a folded fin microchannel heat exchanger operatively and thermally coupled  
5 to the IC die, the folded fin microchannel heat exchanger comprising:  
6           a thermal mass having a cavity;  
7           a folded fin located within the cavity, the folded fin defining, at least  
8 in part, a plurality of microchannels within the cavity; and  
9           an inlet and an outlet, wherein the microchannels are fluidly coupled  
10 at one end to the inlet and at the other end to the outlet;  
11           a pump, having an inlet and an outlet fluidly coupled to the inlet of the  
12 folded fin microchannel heat exchanger; and  
13           a heat rejecter, having an inlet fluidly coupled to the outlet of the folded fin  
14 microchannel heat exchanger and an outlet fluidly coupled to the inlet of the pump,  
15 wherein the system employs a working fluid that transfers heat generated by the IC  
16 die to the heat rejecter using a two-phase heat exchange mechanism.

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1       24. The system of claim 23, wherein the working fluid is water.

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1       25. The system of claim 23, further comprising:  
2           a solderable layer formed on the IC die, wherein the thermal mass is metallic  
3 and wherein the folded fin microchannel heat exchanger is operatively and  
4 thermally coupled to the IC die by the solderable layer.

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1       26. The system of claim 25, wherein the solderable layer and the metallic  
2 thermal mass are made of substantially similar metals.

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1       27. The system of claim 23, wherein the pump comprises an electro osmotic  
2       pump.

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1       28. A system, comprising:

2           an integrated circuit (IC) package;

3           a folded fin microchannel heat exchanger operatively and thermally coupled  
4       to the IC package, the folded fin microchannel heat exchanger comprising:

5               a thermal mass having a cavity;

6               a folded fin located within the cavity, the folded fin defining, at least  
7       in part, a plurality of microchannels within the cavity; and

8               an inlet and an outlet, wherein the microchannels are fluidly coupled  
9       at one end to the inlet and at the other end to the outlet;

10          a pump, having an inlet and an outlet fluidly coupled to the inlet of the  
11       folded fin microchannel heat exchanger; and

12          a heat rejecter, having an inlet fluidly coupled to the outlet of the folded fin  
13       microchannel heat exchanger and an outlet fluidly coupled to the inlet of the pump,  
14       wherein the system employs a working fluid that transfers heat generated by the IC  
15       die to the heat rejecter using a two-phase heat exchange mechanism.

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1       29. The system of claim 28, wherein the working fluid is water.

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1       30. The system of claim 28, further comprising:

2           a solderable layer formed on the IC package, wherein the thermal mass is  
3       metallic and wherein the folded fin microchannel heat exchanger is operatively and  
4       thermally coupled to the IC package by the solderable layer.

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1       31. The system of claim 30, wherein the solderable layer and the metallic  
2       thermal mass are made of substantially similar metals

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1       32.     The system of claim 28, wherein the pump comprises an electro osmotic  
2     pump.

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1       33.     A system comprising:  
2              an integrated circuit (IC) die;  
3              a network interface;  
4              an antenna coupled to the network interface;  
5              a bus, said bus coupling the IC die to the network interface; and  
6              a thermal mass coupled to the IC die, the thermal mass comprising:  
7                  a cavity; and  
8                  a folded fin located within the cavity, wherein the folded fin defines,  
9                  at least in part, a plurality of microchannels within the cavity.

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1       34.     The system of claim 33, further comprising  
2              a solderable layer formed on the IC die; wherein the thermal mass is metallic  
3     and wherein the thermal mass is thermally and operatively coupled to IC die by the  
4     solderable layer.

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1       35.     The system of claim 34, wherein the solderable layer is formed from at least  
2     one of the following metals: copper (Cu), gold (Au), nickel (Ni), aluminum (Al),  
3     titanium (Ti), tantalum (Ta), silver (Ag) and Platinum (Pt).

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1       36.     The apparatus of claim 34, wherein the solderable layer and the metallic  
2     thermal mass are made of substantially similar metals.

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1       37.     The apparatus of claim 33, wherein the thermal mass is thermally and  
2     operatively coupled to the IC die by a thermal adhesive disposed between the  
3     thermal mass and the surface of the IC die.

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1       38.     The apparatus of claim 33, wherein the thermal mass is thermally coupled to  
2     the IC die by a thermal interface material (TIM) layer.

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1       39.     The apparatus of claim 38, further comprising a substrate to which the IC die  
2     is flip-bonded.

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1       40.     The apparatus of claim 39, wherein the thermal mass is operatively coupled  
2     to the substrate via a plurality of fasteners.

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1       41.     The apparatus of claim 40, further comprising a plurality of standoffs  
2     physically coupled to the substrate and to which the plurality of fasteners are  
3     physically coupled.

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1       42.     A system comprising:

2              an integrated circuit (IC) package, said IC package containing one or more  
3     IC dies;

4              a network interface;

5              an antenna coupled to the network interface;

6              a bus, said bus coupling the IC package to the network interface; and

7              a thermal mass coupled to the IC package, the thermal mass comprising:

8                  a cavity; and

9                  a folded fin located within the cavity, wherein the folded fin defines,  
10                 at least in part, a plurality of microchannels within the cavity.

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1       43.     The system of claim 42, further comprising

2              a solderable layer formed on the IC package; wherein the thermal mass is  
3     metallic and wherein the thermal mass is thermally and operatively coupled to IC  
4     package by the solderable layer.

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1       44.     The system of claim 43, wherein the solderable layer is formed from at least  
2     one of the following metals: copper (Cu), gold (Au), nickel (Ni), aluminum (Al),  
3     titanium (Ti), tantalum (Ta), silver (Ag) and Platinum (Pt).

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1       45.     The system of claim 43, wherein the solderable layer and the metallic  
2     thermal mass are made of substantially similar metals.

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1       46.     The system of claim 42, wherein the thermal mass is thermally and  
2     operatively coupled to the IC package by a thermal adhesive disposed between the  
3     thermal mass and the surface of the IC package.

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1       47.     The system of claim 42, wherein the thermal mass is thermally coupled to  
2     the IC package by a thermal interface material (TIM) layer.

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1       48.     The system of claim 47, further comprising a substrate to which the IC  
2     package is flip-bonded.

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1       49.     The system of claim 48, wherein the thermal mass is operatively coupled to  
2     the substrate via a plurality of fasteners.

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1       50.     The system of claim 49, further comprising a plurality of standoffs  
2     physically coupled to the substrate and to which the plurality of fasteners are  
3     physically coupled.

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1       51.     A method; comprising:

2              thermally coupling at least one folded fin microchannel heat exchanger to at  
3     least one IC;

4              passing a working fluid through the at least one folded fin microchannel heat  
5     exchanger;

6           transferring heat produced by the at least one IC via that IC's at least one  
7       folded fin microchannel heat exchanger to the working fluid to convert a portion of  
8       the working fluid passing through the folded fin microchannels in the at least one  
9       folded fin microchannel heat exchanger from a liquid to a vapor phase; and  
10          passing the working fluid exiting the at least one folded fin microchannel  
11       heat exchanger through a heat rejecter, wherein the vapor phase portion of the  
12       working fluid is converted back to a liquid phase.

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1       52.     The method of claim 51, wherein the at least one IC includes a processor IC  
2       and at least one additional IC from the following group: a platform chipset IC, a  
3       video IC, a memory IC and a co-processor IC.

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1       53.     The method of claim 51, wherein the working fluid comprises water.

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1       54.     The method of claim 51, wherein the working fluid is passed through the at  
2       least one microchannel heat exchanger and the heat rejecter via a electro-osmotic  
3       pump.

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1       55.     The method of claim 51, wherein the heat rejecter comprises a channelled  
2     heat sink including a plurality of hollow heat sink fins having respective channels  
3     defined therein.

1       56.     The method of claim 51, wherein the heat rejecter comprises a folded fin  
2     microchannel heat exchanger.